Atty.'s Docket No. 1662-50000 (F98 2426)
Applicants: Thomas J. BONOLA, et al
Title: Method & Apparatus for Eliminating the Software Generated
Ready-Signal to Hardware Devices that are not Part of the Memory
Coherency Domain
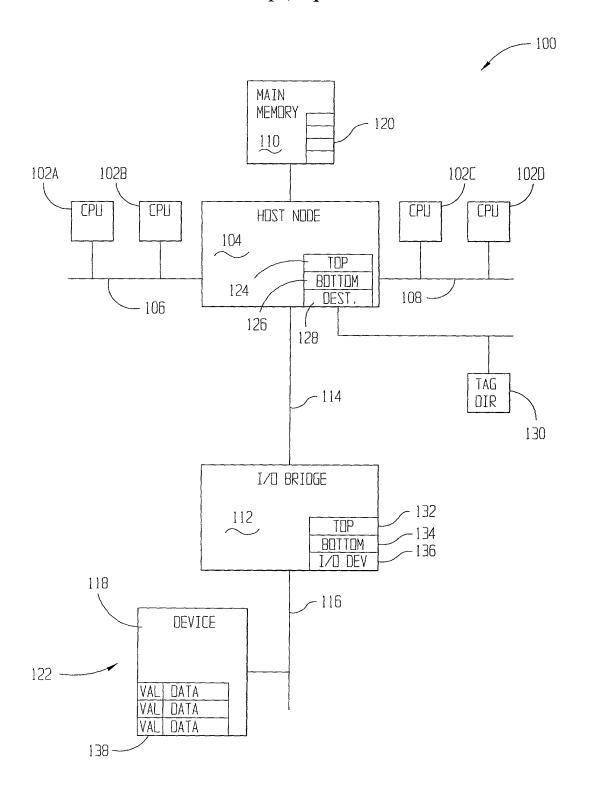Sheet 1 of 1 _ _ _ _ _ _ Express Mail Label No. EV041360238US _
1 / 1

FIG 1